



Efficient encoder design of LDPC code using circulant matrix and eIRA codes

Seul-Ki Bae

sk.bae@coding.yonsei.ac.kr

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Coding & Information Theory Lab.

Department of Electrical and Electronic Engineering, Yonsei Univ.



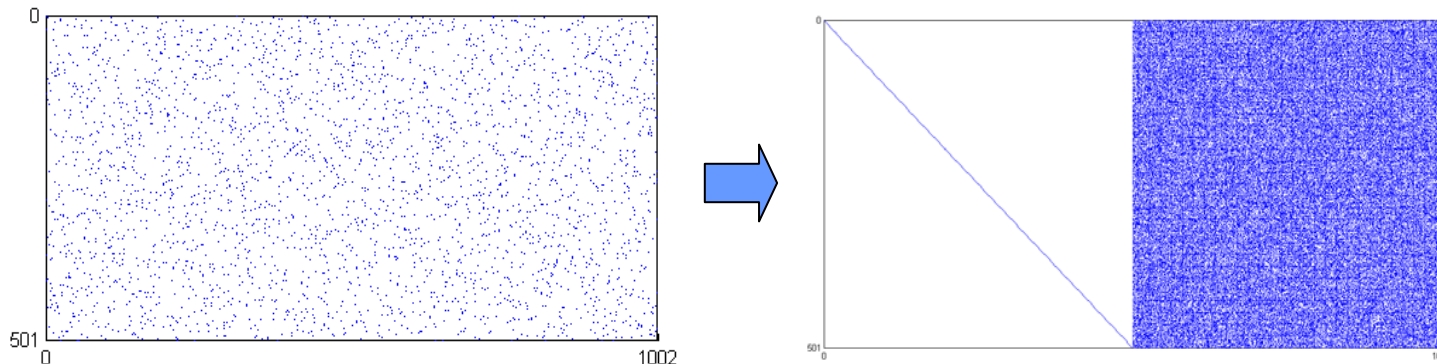
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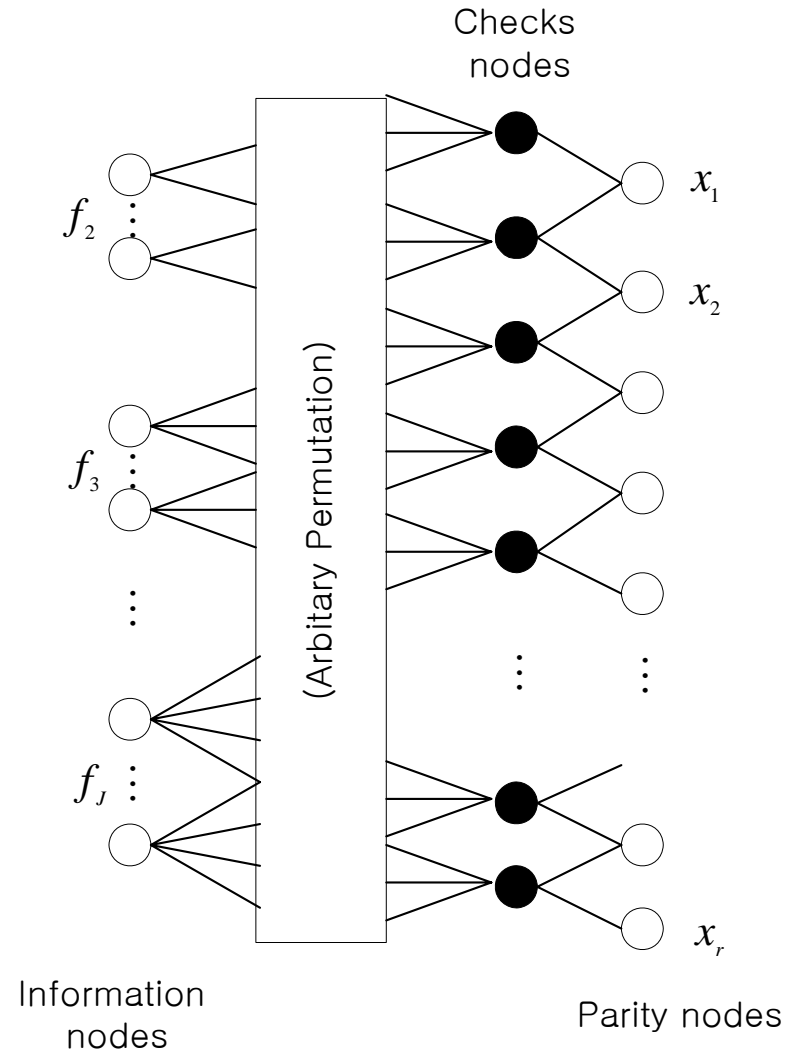
□ Encoding of LDPC codes

- Information bit & Parity bit are computed by matrix multiplication in linear block code.
- 1's in generator matrix are more than parity-check matrix
- Encoding complexity is much high with matrix multiplication operation.



Irregular Repeated-Accumulate (IRA) codes

- k : number of variable nodes
- r : number of checks nodes
- r : number of parity nodes
- Each check node is connected to a information nodes
- The value of parity bit is determined uniquely by the condition that the mod-2 sum of the values of the variable nodes connected to each of the check nodes is zero.



□ Circulant matrix

- H_1 matrix is constructed by $p \times q$ **matrix blocks** which are $l \times l$ **circulant submatrix**, where p is a number of row blocks, q is a number of column blocks.
- Each weight in each circulant submatrix can be different.

$$H_1 = \begin{pmatrix} h_{11} & h_{12} & \cdots & h_{1q} \\ h_{21} & h_{22} & & h_{2q} \\ \vdots & & \ddots & \vdots \\ h_{p1} & h_{p2} & \cdots & h_{pq} \end{pmatrix}$$

- Proposed H_1 matrix can be more modified by **row permutation matrix**.
- Row permutation matrix P is $(n - k) \times (n - k)$ matrix and each rows and columns has only one 1's.
- To prevent from performance degradation, modify to $H_1' = PH_1$ using row permutation matrix.

□ Generator matrix

○ $H = [H_1' \mid H_2] = [H_2^{-1}H_1' \mid I_{n-k}] \rightarrow G = [I_k \mid H_1'^T H_2^{-T}]$

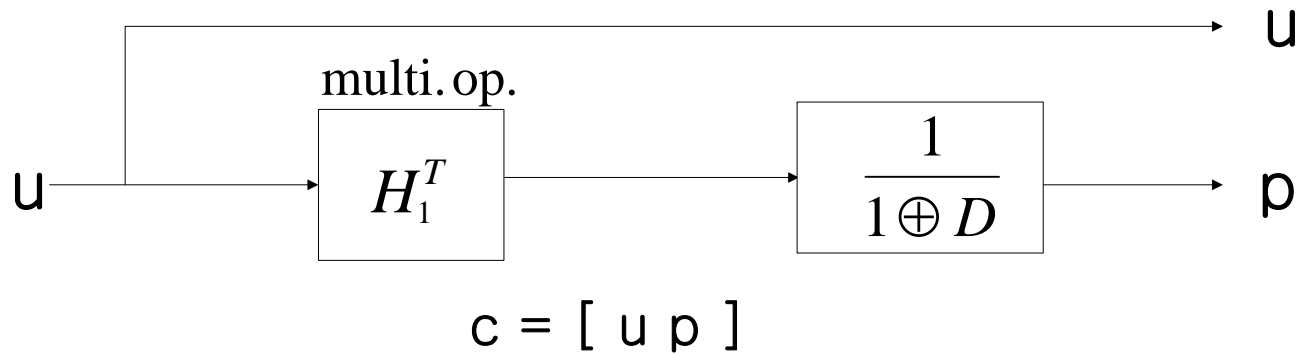
$$G = [I \mid H_1^T P^T H_2^{-T}]$$

$$= \left[\begin{array}{cccc|cccccc} 1 & & & & h_{11}^T & h_{21}^T & \cdots & h_{p1}^T & & & & & & & & & & & 1 & 1 & 1 & 1 & 1 \\ & 1 & & & h_{12}^T & h_{22}^T & \cdots & h_{p2}^T & & & & & & & & & & & & 1 & 1 & 1 & 1 & \\ & & \ddots & & \vdots & \vdots & \ddots & \vdots & & & & & & & & & & & & \cdots & \cdots & \cdots & & \\ & & & 1 & \vdots & \vdots & \ddots & \vdots & \times P^T \times & & & & & & & & & & & \cdots & \cdots & \cdots & & \\ & & & & \vdots & \vdots & \ddots & \vdots & & & & & & & & & & & & & & & 1 & 1 \\ & 1 \\ & 1 \end{array} \right]$$

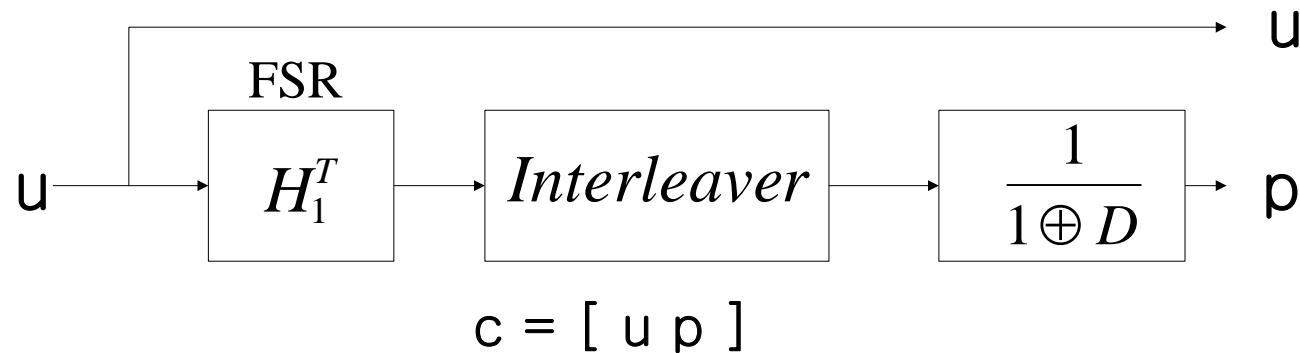
- Since $H_1'^T$ is transposed form of circulant matrix, it can be constructed by shift register and interleaver.
- H_2^{-T} matrix can be constructed by differential encoder.

Encoder Block Diagram

a) Original Encoder

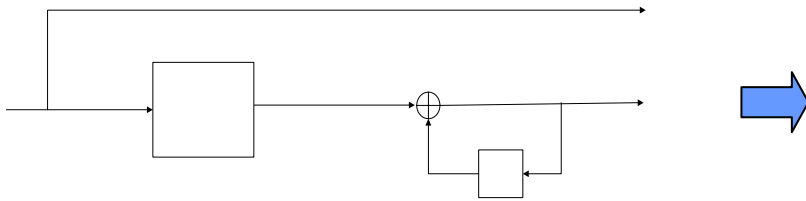


b) Modified Encoder

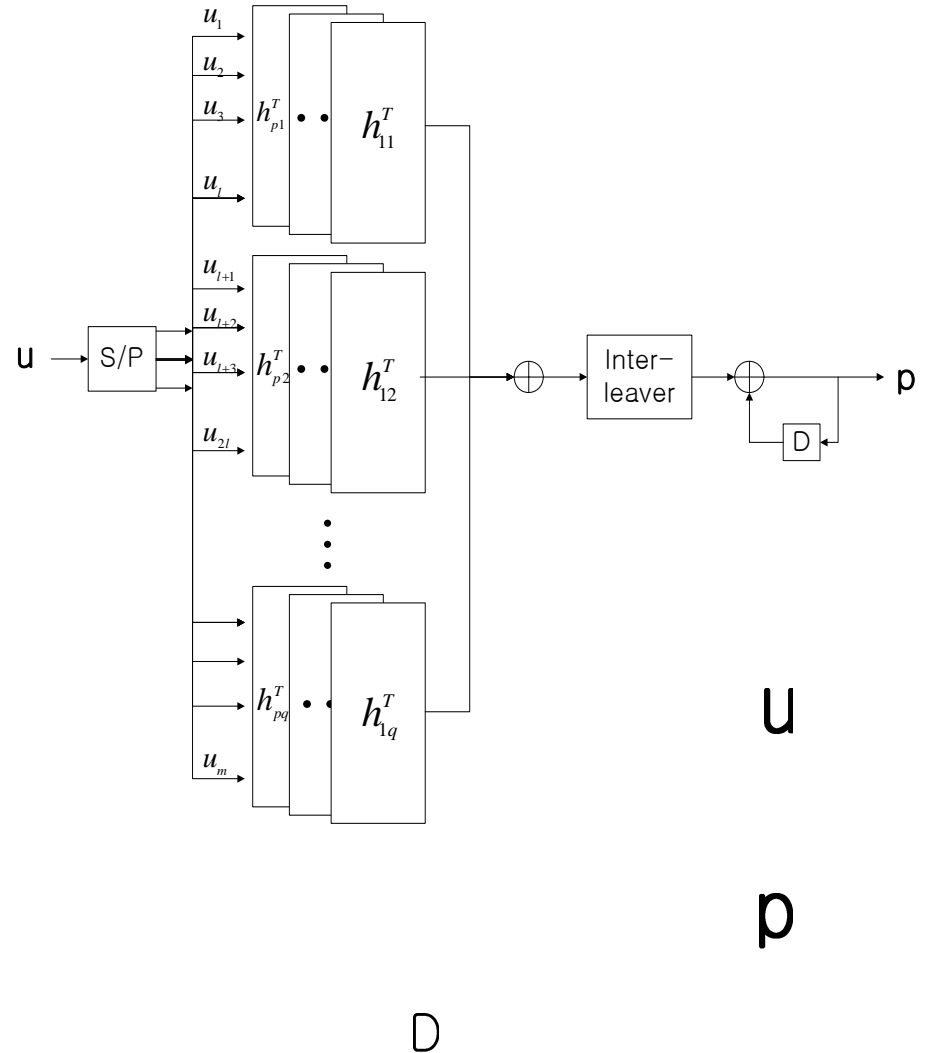
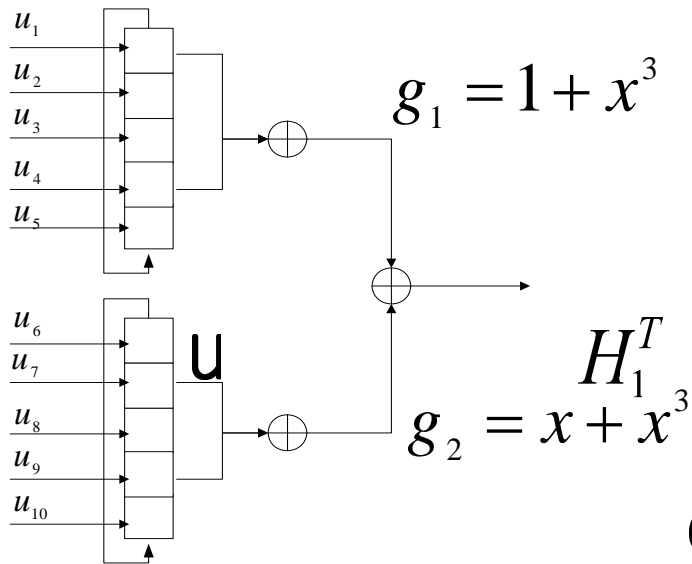


Encoder Structure

- Modified encoder structure



- Example of Shift Register



Complexity Comparison

	δ (general)	Total Computation	Total XOR	Total Memory	Computation comparison	XOR comparison (n=1024, k=768 p=1)
Matrix multiplication	≈ 0.5	$\delta k(n-k)$	$\delta k(n-k)$	n-k	1	•
Michael Yang	≈ 0.01	$\delta(k+1)(n-k)$	$\delta k(n-k)$	n-k	1/50	1
Shift Register	≈ 0.01	$\delta(k+1)(n-k)$	δkp	n-k	1/50	1/256
Shift Register Interleaver	≈ 0.01	$\delta(k+1)(n-k)$	δkp	$\leq 2(n-k)$	1/50	1/256

- δ : density of H_1 Matrix
- Complexity is reduced compared with matrix multiplication operation.
- In our simulation, $(n, k) = (256, 192), (512, 384), (1024, 768), (2924, 2193), p=1, q=3$.



Simulation Results(1/4)

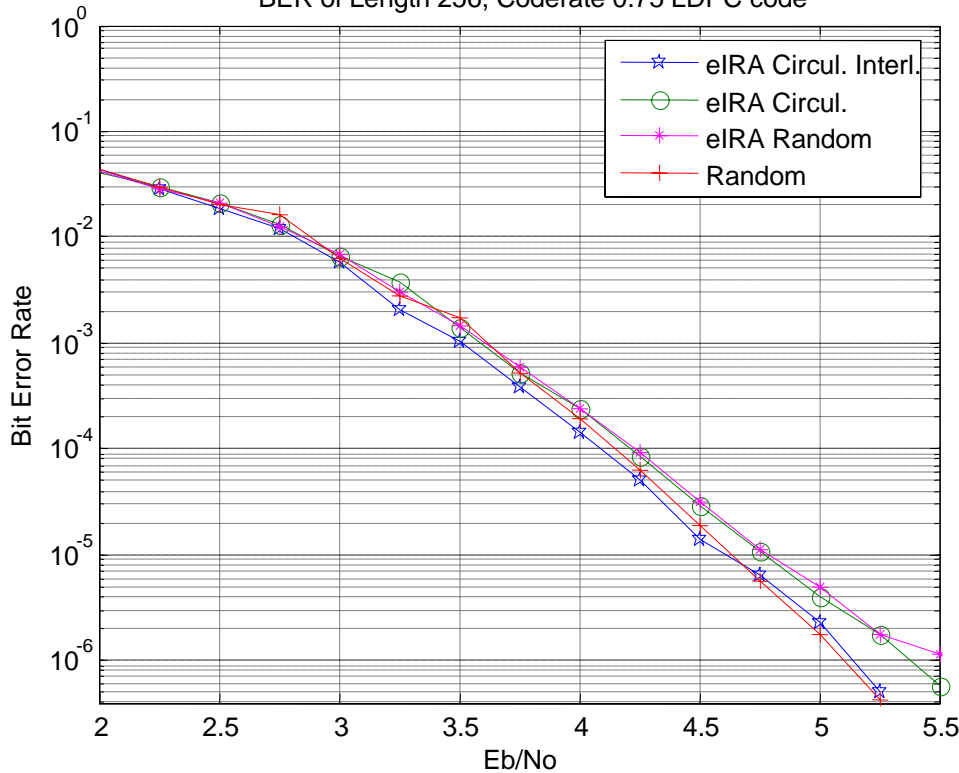


□ Simulation Environments

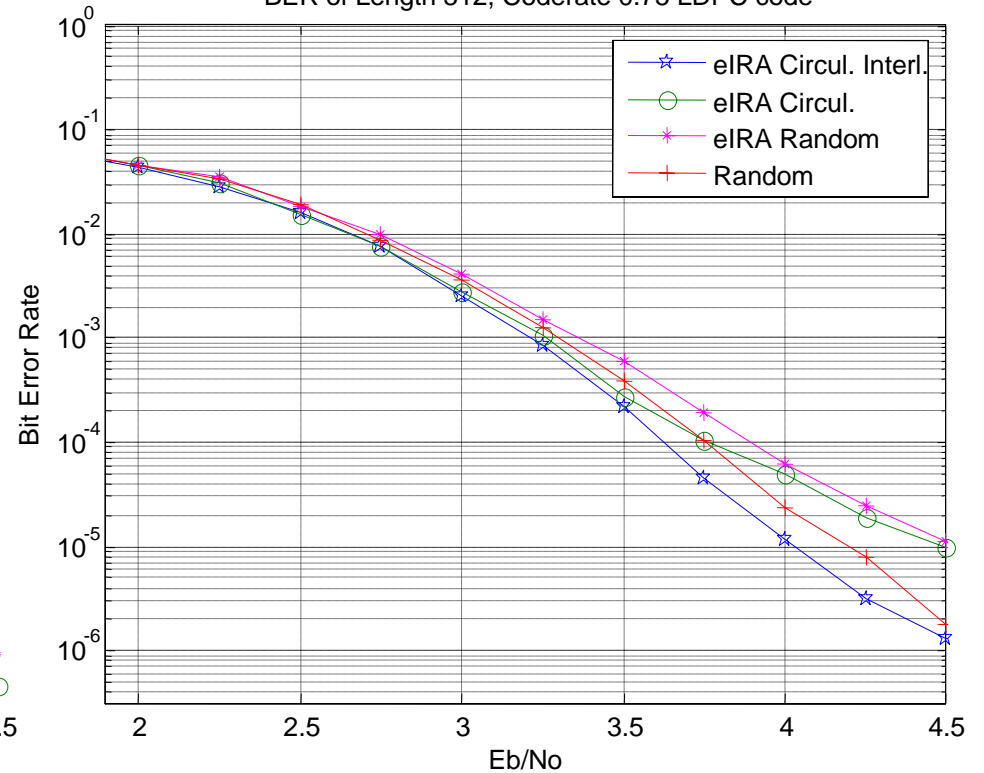
Decoding Algorithm	Sum-Product Algorithm
Maximum Iteration	50
Codeword Length	256, 512, 1024, 2924
Coderate	0.75
Modulation	BPSK
Channel Model	AWGN

Code rate 0.75, length 256, 512

BER of Length 256, Coderate 0.75 LDPC code

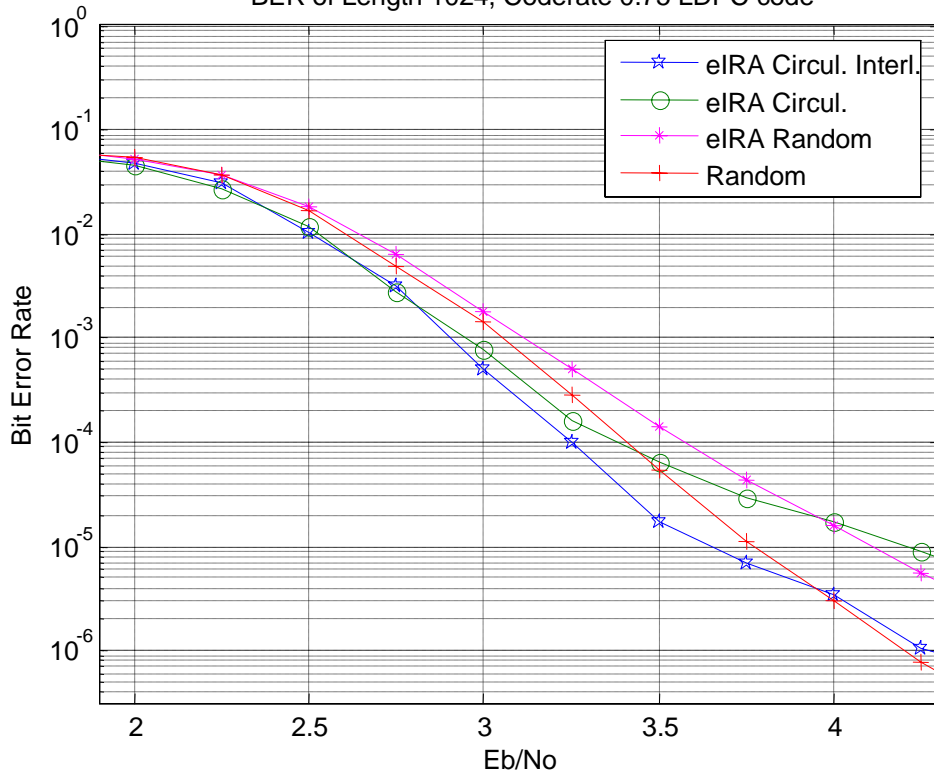


BER of Length 512, Coderate 0.75 LDPC code

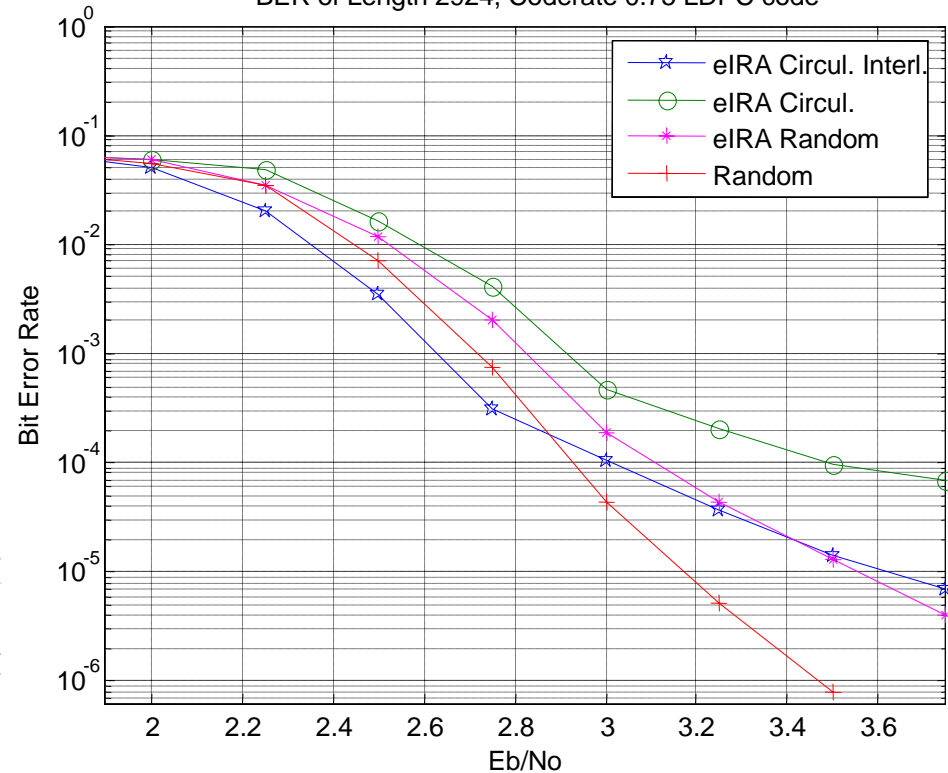


Code rate 0.75, length 1024, 2924

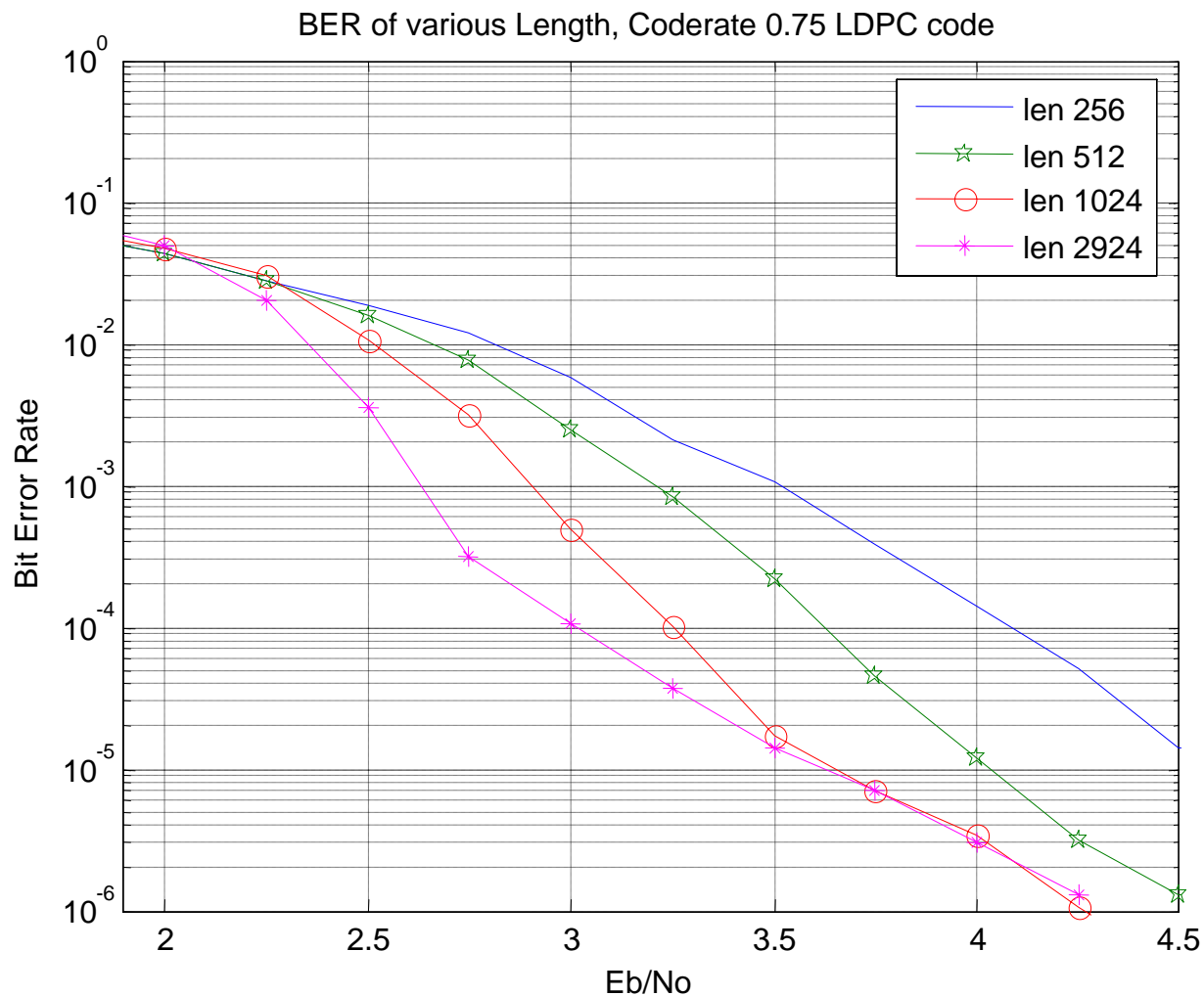
BER of Length 1024, Coderate 0.75 LDPC code



BER of Length 2924, Coderate 0.75 LDPC code



Comparison according to lengths





Concluding Remarks



□ Conclusion

- Encoder complexity is lower than Yang's method.
- Applicable to short length, high rate LDPC code

□ Future Works

- Research on generator polynomial structure to make large girth.
- Research on permutation matrix structure not to make 4-cycle after permutation.