A NEW QUANTIZER SELECTION SCHEME FOR DIGITAL VCR

Seunghyeon Rhee, Yong Je Kim, Jun Geun Jeon, Hong-Yeop Song, and Kyu Tae Park

Abstract---Consumer-Use Digital VCR selects a quantizer for each DCT block based on CLASS and QNO. CLASS is determined for each DCT block according to the characteristic of the block, for example, the maximum value of AC coefficients. Six DCT blocks comprise a macroblock for which a QNO is determined. All of the quantizers defined in the specifications are numbered from ϑ to 15 by QNO. Different quantizers can be referenced by the same QNO and are discriminated by CLASS.

In this paper, we propose a new method to determine these parameters, QNO and CLASS, for the selection of a quantizer per each DCT block. We first determine QNO for a macroblock and then fine-tune the quantizers by adjusting CLASS of each DCT block. For this we propose a modified quantizer map with a reduced set of quantizers, which preserves the compatibility with the conventional one. The results of our experiment show that our method outperforms the conventional method.

I. INTRODUCTION

THE specifications of Consumer-Use Digital VCR [1] are based mainly on the conventional digital image coding techniques already familiarized by such standards as JPEG, MPEG, and H.261, but adopted a far different scheme for quantization of DCT coefficients [2][3][4][5]. The encoder contains a set of quantizers and selects the best suited one for each DCT block. The selection of the quantizer is, therefore, the most important part to increase the coding efficiency. The specifications define several parameters, QNO, CLASS, and AREA, to specify the quantization step size for each coefficient. In this paper, we propose a new method to select a quantizer for Standard Definition(SD) Digital VCR and compare its performance with that of the conventional method. By using the proposed method, we can obtain a significantly improved quality of reconstructed image sequences.

The paper is organized as follows. In Section II, we briefly review the specifications of quantizers for Digital VCR. In Section III, we present a new decision method fully compatible with the specifications, and some experimental results are provided in Section IV. Lastly, in Section V, our conclusion is presented.

II. QUANTIZATION SCHEME FOR STANDARD DEFINITION DIGITAL VCR

The entire image is partitioned into segments and the segments are rearranged to be coded into the bit stream. Each segment consists of 5 macroblocks and each macroblock consists of 6 DCT blocks. There are two modes of DCT provided. The one is 8-8 DCT and the other is 2-4-8 DCT. The latter is similar to the field DCT of MPEG2 [4]. The DC coefficient of each of 6 DCT blocks in a macroblock, together with DCT mode and CLASS of the block, are fixed-length coded on the fixed position. The 30 block areas in a segment, 6 of which are shown in

Byte position number 81 20 21 34 35 48 49 62 63 72 6 MSB s D D D D D D А AC AC AC AC С С С С С lC AC Q 0 2 з 5 N 0 LSB Y0 Y1 Y2 Y3 C_ CB 10 bytes 10 bytes 14 bytes 14 bytes 14 bytes 14 bytes Fig. 1. The structure of a compressed macroblock. 8-8-DCT 2-4-8-DCT DC DC



Fig. 2. The structures of DCT blocks

Fig. 1 for a macroblock, constitute the free space pool of 2,680 bits allocated to the AC coefficients. They are separated by the fixed-positioned data and marked "AC" in Fig. 1. AC coefficients of each DCT block are run-length coded and filled into the free space pool starting from the corresponding block boundary. If the amount of compressed data for a DCT block doesn't overrun its block boundary, the remaining area of the pool is added to the new free space pool. Otherwise, the amount of data exceeding the block boundary is put aside and later taken over to the newly constructed free space pool. A parameter QNO is also fixed-length coded into the header of each macroblock as is marked "QNO" in Fig. 1. Each coefficient in a DCT block is assigned AREA based on its position. In Fig. 2, the higher the frequency of a coefficient is, the higher value of AREA is assigned to it.

The CLASS of a DCT block is determined based on the characteristic of the block. An example presented in the specifications is shown in Table I. The quantization step size for each DCT coefficient is obtained by Table II. Each 4-tuple under AREA defines a quantizer, which is selected by a pair (CLASS, QNO), where QNO ranges from 0 to 15 and CLASS from 0 to 3. If the compressed AC coefficients overflow the allowed capacity, they are cut off. On the other hand, if they doesn't use up the allowed capacity, the remaining area can be considered filled with dummy bits.

S. Rhee, J. G. Jeon, H. Y. Song, and K. T. Park are with the Department of Electronic Engineering, Yonsei University.

Y. J. Kim is with the Signal Processing Lab., Corporate Technical Operations, Samsung Electronics Co., Ltd.

	Maximum absolute value of AC coefficients							
	0 to 11	12 to 23	24 to 35	> 35				
Y	0	1	2	3				
CR	1	2	3	3				
CB	2	3	3	3				

TABLE I									
AN EXAMPLE OF THE CLASSIFICATION									



QUANTIZER MAP

In this process, there may be two criteria for determining the best quantizer for a DCT block. The first scheme allows a small amount of encoded data bits to overflow the bit budget, which enables the lower frequency components to be quantized using smaller step size (hence, gives better quality of the reconstructed image) at the price of cutting off small amount of higher frequency components. The second scheme, on the other hand, puts stronger restriction on the amount of encoded data bits not to exceed the bit budget, 2,680bits/segment, which preserves all the frequency components but in a relatively larger quantizer step size. It would be arguable which scheme, in general, performs better, and it is outside the scope of this paper. However, the choice should be easily made after the quantizer selection scheme is designed.

To determine the quantizer, all of the 16 QNOs are tested. In general, this procedure can be performed by the following procedure:

- A segment-wide QNO is selected; and
- QNO is adjusted for each macroblock.

Since CLASS for each DCT block has already been determined, we can perfectly describe a quantizer for every DCT block in the macroblock by choosing a QNO for a macroblock. Finally, the selected QNOs are tested again to check if it can be adjusted to the next higher value without the coded data overflowing the



Fig. 3. The procedure and expected result of adjusting QNO

	CLASS				QID			
	1	2	3	0	1	2	3	
QNO	12/14	15		1	1	1	1	0
		14		1	1	1	2	1
	10	12		1	1	2	2	2
	8	10		1	2	2	4	3
	6	8		2	2	4	4	4
	4	6		2	4	4	8	5
	2	4		4	4	8	8	6
	0	2		4	8	· 8	16	7
		0		8	8	16	16	8
			0	16	16	32	32	9

TABLE III Proposed quantizer map with new parameter QID

bit budget. This fine-tuning procedure is illustrated in Fig. 3.

III. THE PROPOSED METHOD

There are two parameters we can control to get a better quantizer—QNO and CLASS. In the conventional method, the values of CLASSes are predetermined and QNOs are selected afterwards. This makes the adjustment of the quantizers more or less large-grained, because the QNO is applied to all the DCT blocks in a given macroblock. The unused areas even after the adjustment are considered as dummy bits and ignored at the decoding process. To reduce the dummy bits filling the unused space, a fine-grained scheme of choosing quantizer is desirable.

In fact, only 11 unique quantizers are defined in the specifications. Many of them appear twice or even more, referenced by a unique address made up of a pair (CLASS, QNO). For example, (2, 15) and (1, 14) refer to the same quantizer. We can erase the doubly-used entries and rebuild the quantizer map as shown in TABLE III. We name the singly-used quantizers as QID0 through QID9.

It can be found that given a fixed QNO, say 10, we can change the quantizer from QID3 to QID2, only by lowering the CLASS of the DCT block from 2 to 1. Using this relation, the alternative procedure of selecting the quantizers can be described as follows:

- A segment-wide QID is selected; and
- CLASS is adjusted for each DCT block.

The QIDs are essentially a reduced set of all the possible quantizers, and each QID is only a temporary parameter, and later converted into corresponding QNO to be coded into the bit stream. The sum of all 30 DCT blocks of a segment is first

DCT Block	0	1	2	3	26	27	28	29	SUM
0									
1									
2									
3	1	♠							
4									75
5									
9									

Fig. 4. The proposed procedure of adjusting CLASSes



Fig. 7. Mobile Calendar



Fig. 5. Flower Garden



Fig. 8. Popple

IV. SIMULATION

checked for QID0 through QID9 to determine the highest QID that does not make the compressed AC component exceed the segment buffer. The initial CLASSes are preset to 2, and at the second stage they are tested again if they can be lowered without overflowing the segment buffer. This method is shown in Fig. 4.

The simulations are performed with 525-60 system defined in the specifications, and the first frames of 5 test sequences used in our experiments are shown in Fig. 5, 6, 7, 8, 9, with 720×480 pixels in each frame. In the conventional method, we determine CLASS of each DCT block according to the example presented



Fig. 6. Football





Fig. 10. Results for 50 frames in *Flower Garden* sequence. The solid lines represent the results of the proposed method and the dotted lines represent those of the conventional method.



Fig. 11. Results for 50 frames in *Football* sequence. The solid lines represent the results of the proposed method and the dotted lines represent those of the conventional method.



Fig. 12. Results for 50 frames in *Mobile Calendar* sequence. The solid lines represent the results of the proposed method and the dotted lines represent those of the conventional method.



Fig. 13. Results for 50 frames in *Popple* sequence. The solid lines represent the results of the proposed method and the dotted lines represent those of the conventional method.



Fig. 14. Results for 50 frames in *Table Tennis* sequence. The solid lines represent the results of the proposed method and the dotted lines represent those of the conventional method.

in the specifications, which is shown in TABLE I. For all of the test sequences, it is clear that the proposed method provides much improved performance in both Y and C_B components. In Fig. 13, the proposed method shows higher PSNR of Y component by about 3 dB throughout the tested 50 images of *Popple* sequence. C_R components, however, show slightly lower quality than those obtained by the conventional method in some sequences. This is probably due to the unfairness in treating C_B and C_R components as defined in TABLE I. This "unfairly treating" policy may lead to better quality for C_R components, which in turn may lead to the degradation of C_B components. The proposed method is designed based on equal treatment of these two components.

V. CONCLUSION

We presented a new method to select a quantizer for each DCT block in Digital VCR. It is fully compatible with the specifications. It's advantage comes mainly from the reduction of dummy bits or unused area in the compressed video segments. This was achieved by restructuring the quantizer map in the specifications into a much simpler version and introducing a new temporary parameter QID. Simulation confirms the much better performance of the proposed method compared with the conventional scheme.

To overcome the problem of somewhat degraded quality of C_R components, we believe that more research and simulations are needed based on the human visual system, especially on the sensitivity to color components. Finally, a development of a cost-effective architecture well suited for implementation of this method remains as a future work.

References

- [1] HD Digital VCR Conference, Specifications of Consumer-Use Digital VCRs, Dec. 1994.
- [2] W. B. Pennebaker and J. L. Mitchell, Still Image Data Compression Standard, Van Nostrand Reinhold, New York, 1993.
- [3] ISO/IEC JTC1/SC29/WG11, ISO/IEC 11172-2 Draft International Standard, March 1992.
- [4] ISO/IEC JTC1/SC29/WG11, Recommendation ITU-T H.262 ISO/IEC 13818-2 International Standard, Jan. 1995.
- [5] CCITT, Recommendation H.261: Video codec for audiovisual services at $p \times 64kbits/s$, 1990.



Seunghyeon Rhee was born in Scoul, Korea, in 1969. He received his B.S. and M.S. degrees from Yonsei University, Scoul, Korea in 1993 and 1995, respectively. He is a candidate for a Ph.D. in electronic engineering. He is a research assistant at the Center for Signal Processing Research, Yonsei University. His research interests include image coding, image restoration, and multimedia systems. He is a student member of IEEE and a member of Korea Institute of Telematics and Electronics.



Yong Je Kim was born in Chinhae, Korea, in 1962. He received B.S. degree in electronic engineering from Sogang University in 1985 and M.S. in electronic engineering from Ajou University in 1994. He joined Samsung Electronics Co., Ltd. in 1985 and he is currently working on the digital image processing.



Jun Geun Jeon was born in Seoul, Korea, in 1970. He received B.S. and M.S. degrees all in electronics engineering, from Yonsei University, Seoul, Korea, in 1993 and 1995, respectively. He is currently working toward Ph.D. degree at Yonsei University. His research interests include motion tracking, stereo matching, and video data compression.



Hong-Yeop Song received his B.S. degree in Electronic Engineering from Yonsei University in 1984, MSEE and Ph.D. degrees from University of Southern California, Los Angeles, CA in 1986 and 1991, respectively, specializing in the area of communication theory and coding. After spending 2 years as a research staff in Communication Sciences Institute at USC working with Dr. Solomon W. Golomb from 1992 to 1993, he joined Qualcomm Inc., San Diego, CA in 1994 as a senior engineer and worked in a team researching and developing CDMA-PCS Stan-

dards for North America. He joined the Dept. of Electronic Engineering at Yonsei University, Seoul, Korea in 1995 where he is currently working as an assistant professor. His area of interest is the application of discrete mathematics into various communication and coding problems. He is a member of IEEE, Mathematical Association of America, and Korea Institute of Telematics and Electronics.



Kyu Tae Park received his B.S. in electrical engineering from Yonsei University in 1957, M.Sc. in engineering from University College, London, and a Ph.D. from Southampton University, England, in 1964 and 1969, respectively. He joined the Department of Electronic Engineering at Yonsei University, Seoul, Korea, in 1969 where he is currently a professor and the Director of the Center for Signal Processing Research. He also served as the Dean of the College of Engineering. Prof. Park was appointed to the Presidential Council for Science and Technology, Korea, and he is

on the Board of Directors of the Korea Science and Engineering Foundation. He has published more than 150 research articles and authored 10 books. His research interests include image coding, computer vision, and high-speed computer architecture. He is a member of IEEE, SPIE, and a fellow of IEE.